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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/731,253	12/09/2003	Joseph M. Freund	FREUND 75-23	4712
27964	7590	12/13/2005	EXAMINER	
HITT GAINES P.C. P.O. BOX 832570 RICHARDSON, TX 75083				DOAN, JENNIFER
			ART UNIT	PAPER NUMBER
			2874	

DATE MAILED: 12/13/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/731,253	FREUND ET AL.	
	Examiner	Art Unit	
	Jennifer Doan	2874	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 25 November 2005.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) 11-20 is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-6 and 8-10 is/are rejected.
- 7) Claim(s) 7 is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ . |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____ . |

DETAILED ACTION

Election/Restrictions

1. Applicant's election of Group I in the reply filed on November 25, 2005 is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)).

Drawings

2. The drawings, filed on 12/09/03, are accepted.

Specification

3. Applicants' cooperation is requested in correcting any errors of which applicants may become aware in the specification.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States

only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 1, 2, 4-6, 8 and 10 are rejected under 35 U.S.C. 102(e) as being anticipated by Kochi et al. (U.S. Patent 6,483,954).

With respect to claim 1, Kochi et al. (figure 1) disclose an optoelectronic device, comprising an optical substrate (103) coupled to a submount (109) and including an optical device (134); and a signal (121) and a ground conductor (158) coupled to the submount (109), wherein the signal and ground conductors (121, 158) have a direction of signal propagation associated therewith, and wherein related transitions of the signal and ground conductors (121, 158) between the submount (109) and the optical substrate (103) are separated along the direction by a predetermined distance.

With respect to claim 2, Kochi et al. (figure 1) disclose the optoelectronic device, wherein the related transitions are transitions of the signal and ground conductors (121, 158) from the submount (109) to the optical substrate (103).

With respect to claim 4, Kochi et al. (figure 1) disclose the optoelectronic device, wherein the optical device includes an optical waveguide (column 3, lines 12-13) having a modulation region, and wherein one of the transitions is adjacent the modulation region.

With respect to claim 5, Kochi et al. (figure 1) disclose the optoelectronic device, wherein the related transitions are first transitions, and further including second related transitions of the signal and ground conductors (119, 121, 158) between the submount (109) and the optical substrate (103), wherein the second transitions are separated along the direction.

With respect to claim 6, Kochi et al. (figure 1) disclose the optoelectronic device, wherein the optical device (134) is a modulator.

With respect to claim 8, Kochi et al. (figure 1) disclose the optoelectronic device, wherein the predetermined distance defines a region (105) that is at least a portion of an inductive element.

With respect to claim 10, Kochi et al. (figure 1) disclose the optoelectronic device, wherein the optoelectronic device is included within an optical communications system including a transmitter or a receiver.

Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

7. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to

consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

8. Claims 3 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kochi et al. (as cited above).

With respect to claims 3 and 9, Kochi et al. substantially disclose all the limitations of the claimed invention except the predetermined distance is between about 10% - 100% of radian of phase of shortest wavelength and between about 1-30 μm .

However, the predetermined distance between about 10% - 100% of radian of phase of shortest wavelength and between about 1-30 μm are considered to be obvious. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the predetermined distance between the submount and substrate of Kochi's device within the ranges as claimed for the purpose of obtaining the better arrangement of the optical signal transmission elements, and it also has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art and it is noted that the applicant does not disclose criticality in the ranges claimed. *In re Aller*, 105 USPQ 233 (see MPEP § 2144.05).

With respect to claim 9, Kochi et al. substantially disclose all the limitations of the claimed invention except for a material having a dielectric constant less than about 4.0 occupied the gap.

However, the a material having a dielectric constant less than about 4.0 occupied the gap is considered to be obvious. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the gap between the submount and the substrate of Kochi's device with the material having a dielectric constant with the value as claimed for the purpose of obtaining the better arrangement of the optical signal transmission elements, and it also has been held that discovering an optimum value of a result effective variable involves only routine skill in the art and it is noted that the applicant does not disclose criticality in the value claimed.

In re Boesch, 617 F.2d 272, 205 USPQ 215 (CCPA 1980) (see MPEP § 2144.05).

Allowable Subject Matter

9. Claim 7 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art of record fails to disclose or reasonably suggest the optoelectronic device further including a coplanar waveguide and a load resistor, the coplanar waveguide located on the submount and including a signal trace and a ground trace respectively contacting the signal and ground conductors, the load resistor coupled to the signal conductor at an end opposite from the signal trace, wherein a distance (d_1+d_2) between the signal trace and the or equal to the shortest wavelength the signal and ground conductors.

Conclusion

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer Doan whose telephone number is (571) 272-2346. The examiner can normally be reached on Monday to Thursday from 6:00 am to 3:30 pm, second Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rodney Bovernick can be reached on (571) 272-2344. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Jennifer Doan

Patent examiner

December 1, 2005